

The diagram illustrates a cross-sectional view of a semiconductor device. At the base is a substrate (7) with a central block (2) and two side blocks (3). Above this is a layer (6) containing a series of vertical columns of circular elements (10). These columns are separated by regions (11) and (12). The columns are connected to a series of horizontal lines (14, 15, 16) which are labeled with voltages  $V_s I$ ,  $V_s II$ , and  $V_s III$ . The diagram also shows various other components like 8, 9, 13, 14a, 14b, 15a, 15b, 16a, and 16b, and regions labeled S I, S II, and S III.

Fig. 2